**RX12864C2** Graphic 128x64 dots

### Mechanical Data

<table>
<thead>
<tr>
<th>Item</th>
<th>Standard Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Module Dimension</td>
<td>55.2 x 39.8</td>
<td>mm</td>
</tr>
<tr>
<td>Viewing Area</td>
<td>45.2 x 27.0</td>
<td>mm</td>
</tr>
<tr>
<td>Dot Pitch</td>
<td>0.32 x 0.38</td>
<td>mm</td>
</tr>
<tr>
<td>Dot Size</td>
<td>0.28 x 0.34</td>
<td>mm</td>
</tr>
</tbody>
</table>

### Electrical Characteristics

<table>
<thead>
<tr>
<th>Item</th>
<th>Symbol</th>
<th>Standard Value</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage</td>
<td>VDD</td>
<td>3.0</td>
<td>V</td>
</tr>
<tr>
<td>Recommended LCD Driving Voltage for Normal Temp. Version module @25ºC</td>
<td>Vo-Vss</td>
<td>9.60</td>
<td>V</td>
</tr>
</tbody>
</table>

### Feature

1. Built-in controller ST7565P or Equivalent
2. 3.3V power supply
3. 1/64 duty cycle
4. Interface: 6800/8080/SPI

### Pin No. | Symbol | Description
---|--------|-----------------------------------
1 | IRS | Selects the resistors for the V0 voltage level adjustment
2 | /HPM | Control power supply for LCD: “H”: Normal mode, “L”: High power mode
3 | P/S | Selects the interface type: Serial or Parallel
4 | C86 | Interface selection 6800/8080
5 | VR | LCD Contrast Adjustment when IRS = “L”
6-10 | V0-V4 | Bias voltage levels for LCD driving
11 | VRS | This is the internal-output VREG power supply for the LCD power supply voltage regulator.
12 | CAP4+ | DC/DC voltage converter.
13 | CAP2- | DC/DC voltage converter.
14 | CAP2+ | DC/DC voltage converter.
15 | CAP1+ | DC/DC voltage converter.
16 | CAP1- | DC/DC voltage converter.
17 | CAP1- | DC/DC voltage converter.
18 | CAP4- | DC/DC voltage converter.
19 | Vout | Positive Voltage output
20 | Vss | Ground
21 | Vdd | Power supply for logic
22-29 | D0-D7, D7-D0 | #N/A
30 | /RD/E | 6800 family: Read signal, 6800 family: Enable clock
31 | WRR/WRO | 6800 family: Write signal, 6800 family: Read/Write signal
32 | A0 | Data/Instruction select signal
33 | /RES | Controller reset signal, Active Low
34 | /CS1 | This is the chip select signal.